

IN THE CLAIMS:

The instant amendment cancels claims 27, 29 and 42-57 without prejudice or disclaimer, and adds claims 58-81. After the entry of the instant amendment, the claims will be:

Claims 1-57 (cancelled).

58. (new) A data delivery system comprising:

- a memory cartridge including a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S, the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank;

- a server configured to deliver content units, through a network, each content unit having a respective title;

- a writer including a connector that is connectable to the terminals of the memory cartridge; and

- a controller configured to

- receive a title from the server,

- send the title to a display, to allow a user to view the title,

- receive an input from the user,

- receive a content unit delivered from the server via the network, the

- received content unit having the title viewed by the user, and

- cause the writer to write the received content unit to the semiconductor memory,

wherein the writer is configured to write the received content unit to the semiconductor memory, through the connector, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not subsequently written to the single one of the memory areas.

59. (new) The data delivery system according to claim 58 wherein the each content unit includes a music piece.

60. (new) The data delivery system according to claim 58 wherein each memory area has a corresponding status area in the semiconductor memory, and the writer is configured to write a first state to a status area before writing to the corresponding memory area, and subsequently, to conditionally write a second state to the status area depending on whether the received content unit is normally written to the corresponding memory area.

61. (new) The data delivery system according to claim 60 wherein the corresponding status area is non-contiguous with the memory area.

62. (new) The data delivery system according to claim 60 wherein the writer is configured to conditionally write the received content unit whose write operation was interrupted to a memory area depending on whether the corresponding status area indicates the first state.

63. (new) The data delivery system according to claim 62 wherein the writer is configured to conditionally write to the memory area depending on whether the received content unit whose write operation was interrupted is saved in a storage device.

64. (new) The data delivery system according to claim 62 wherein the writer is configured to conditionally write to a memory area depending on whether identification information of the memory cartridge being currently connected to the writer corresponds to identification information saved in a storage device when a write operation is interrupted.

65. (new) The data delivery system according to claim 58 further including:
a microphone type karaoke device,
wherein the memory cartridge is inserted into the microphone type karaoke device, and

wherein each content unit includes karaoke data that contains lyric text data, and score data.

66. (new) The data delivery system according to claim 65 wherein the writer further includes:

a slot for inserting the memory cartridge,

wherein the memory cartridge as inserted into the slot is manually detachable by the user.

67. (new) The data delivery system according to claim 66 wherein the writer further includes a housing, and the slot is formed in an upper surface of the housing.

68. (new) The data delivery system according to claim 65 wherein the writer is implemented within the microphone type karaoke device.

69. (new) A writer configured to operate with a system including a memory cartridge including a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S, the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank; a server configured to deliver content units, through a network, each content unit having a respective title; and a controller including a first processor, the first processor being configured to receive a title from the server, send the title to a display, to allow a user to view the title, receive an input from the user, receive a content unit delivered from the server via the network, the received content unit having the title viewed by the user, and send the received content unit to the writer, the writer comprising:

- a connector that is connectable to the terminals of the memory cartridge, and
- a second processor configured to

- receive the content unit sent from the first processor, the received content unit having the title viewed by the user, and

- write the received content unit to the semiconductor memory, through the connector, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not subsequently written to the single one of the memory areas.

70. (new) The writer according to claim 69 wherein each memory area has a corresponding status area in the semiconductor memory, and the second processor is configured to write a first state to a status area before writing to the corresponding memory area, and to conditionally write a second state to the status area depending on whether the received content unit is normally written to the corresponding memory area.

71. (new) The writer according to claim 70 wherein the corresponding status area is non-contiguous with the memory area.

72. (new) The writer according to claim 70 wherein the second processor is configured to conditionally write the received content unit whose write operation was interrupted to a memory area depending on whether the corresponding status area indicates the first state.

73. (new) The writer according to claim 72 wherein the second processor is configured to conditionally write to the memory area depending on whether whose write operation was interrupted is saved in a storage device.

74. (new) The writer according to claim 72 wherein the second processor is configured to conditionally write to a memory area depending on whether identification information of the memory cartridge being connected to the writer currently corresponds to identification information saved in a storage device when a write operation is interrupted.

75. (new) A data memory cartridge configured to operate with a system including a server configured to deliver content units, through a network, each content unit having a respective title; a writer including a connector that is connectable to the terminals of the memory cartridge; and a controller being configured to receive a title from the server, send the title to a display, to allow a user to view the title, receive an input from the user, receive a content unit delivered from the server via the network, the received content unit having the title viewed by the user, and cause the writer to write the received content unit to the memory cartridge, the data memory cartridge comprising:

a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S, the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank, enabling the received content unit to be written to the semiconductor memory, through the connector, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not subsequently written to the single one of the memory areas.

76. (new) The data memory cartridge according to claim 75 wherein each memory area has a corresponding status area in the semiconductor memory, enabling the the writer to write a first state to a status area before writing to the corresponding memory area, and to conditionally write a second state to the status area depending on whether content is normally written to the corresponding memory area.

77. (new) The data memory cartridge according to claim 76 wherein the corresponding status area is non-contiguous with the memory area.

78. (new) The data memory cartridge according to claim 76 wherein the writer is enabled to conditionally write the received content unit whose write operation was interrupted to a memory area depending on whether the corresponding status area indicates the first state.

79. (new) A server configured to operate with a system including a memory cartridge including a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S, the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank; a writer including a connector that is connectable to the terminals of the memory cartridge, and a processor, the server comprising:

- a first transmitter that sends a title to the processor;

- a receiver that receives an input from the user;

- a second transmitter, responsive to the receiver, that sends a content unit to the processor, via the network, the sent content unit having the title viewed by the user, to enable the processor to cause the writer to write the sent content unit to the semiconductor memory, through the connector, wherein the writer is configured to write the received content unit to the semiconductor memory, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not

subsequently written to the single one of the memory areas.

80. (new) A computer-readable medium storing a computer program that enables a computer to perform a process, the computer for operating with a data delivery system having a memory cartridge including a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S , the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank; a server configured to deliver content units, through a network, each content unit having a respective title; a writer including a connector that is connectable to the terminals of the memory cartridge, the process comprising:

- receiving a title from the server;

- sending the title to a display, to allow a user to view the title;

- receiving an input from the user;

- receiving a content unit from the server, via the network, the received content unit having the title viewed by the user; and

- causing the writer to write the received content unit to the semiconductor memory, wherein the writer is configured to write the received content unit to the semiconductor memory, through the connector, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not subsequently written to the single one of the memory areas.

81. (new) A computer-readable medium storing a computer program that enables a server to perform a process, the server configured to operate with a system including a memory cartridge including a package having terminals, and a semiconductor memory, the semiconductor memory being initially set with N memory areas each of size S , the number N of the memory areas corresponding to a number of content units that can be written to the memory cartridge, the N memory areas being initially set to be blank; a writer including a connector that is connectable to the terminals of the memory cartridge,

and a processor, the process comprising:

- sending a title to the processor;

- receiving an input from the user via the processor;

- sending a content unit to the processor, via the network, the sent content unit having the title viewed by the user, to enable the processor to cause the writer to write the sent content unit to the semiconductor memory, through the connector, wherein the writer is configured to write the received content unit to the semiconductor memory, such that the received content unit is written to only a single 1 of the memory areas, and other content units are not subsequently written to the single one of the memory areas.